

### Remarks

In the subject action, claims 1-30 were rejected under 35 USC § 103(a) as being unpatentable over Westerinen et al. (US Patent No. 2004/0088589), and in further view of Cooper et al. (US Patent No. 5,838,982). Claims 8-12 and 20-24 are objected to based on minor informalities. The applicant respectfully disagrees with the examiner, therefore requested reconsideration for at least the reasons set forth.

Regarding claims 1, 8, 13, 20, 26 and 29, Westerinen speaks of the ON\_BATT signal indicating a culmination of AC power absence and the battery has sufficient energy [e.g. paragraph 0027]; Westerinen, however, fails to speak of a signal indicating of an AC absence condition only. Cooper merely teaches of verifying of system power availability when the power button is pressed, and further continue to check other power source in the absence of system power [e.g. figure 2, column 3, lines 37-38, and column 3, lines 41-43]. Cooper fails to teach of AC power, in its place Cooper speaks of an AC adapter [e.g. column 1, line 15]. To one ordinarily skills in the art, AC adapter would suggest of a supply of DC power to the computer, the exact opposite of AC power. Cooper further fails to speak of negating the power button event signal, alternatively the system merely “END”s when all power sources is not available [e.g. figure 2, step 114]. Moreover, applicant reverently disagrees with the examiner in regards to “ignoring” an event signal versus “negating” an event signal. In this context, applicant respectfully suggests that “ignore” implies of refusal of consideration; thus, no corresponding process or element [e.g. figure 2, step 112, the system simply “END”s]. Whereas negation suggests of reversing the polarity of the event signal, implies at least of a process or an element for negation. Therefore, even collectively, Cooper and Westerinen merely instructs of combination of (1) a signal indicating of a sufficient power source (battery) and an AC failure condition, and (2) that the power button is pressed and the system power source is verified.

Applicant fails to see how the above said teaching of Westerinen combined with Cooper, would suggest to one of ordinary skill in the art of the following citation comprising:

- receiving or generating a state signal signalling of the state of an AC failure, presence or absence;
- receiving a power button or wake event signal associated with a power button or a wake event of the apparatus; and
- negating the said event signal if the state signal indicates AC failure or AC absence state.

For at least the above reasons claims 1, 8, 13, 20 26 and 29 are patentable over Westerinen, even if combined with Cooper.

Claims 2-7 depend and add to on claim 1, with all its recitation. For at least the same reason as claim 1, claims 2-7 are therefore also patentable over Westerninen, and in further view of Cooper.

Claims 9-12 depend and add to on claim 8, with all its recitation. For at least the same reason as claim 8, claims 9-12 are therefore also patentable over Westerninen, and in further view of Cooper.

Claims 14-19 depend and add to on claim 13, with all its recitation. For at least the same reason as claim 13, claims 14-19 are therefore also patentable over Westerninen, and in further view of Cooper.

Claims 21-25 depend and add to on claim 20, with all its recitation. For at least the same reason as claim 20, claims 21-25 are therefore also patentable over Westerninen, and in further view of Cooper.

Claims 27-28 depend and add to on claim 26, with all its recitation. For at least the same reason as claim 26, claims 27-28 are therefore also patentable over Westerninen, and in further view of Cooper.

Claim 30 depends and adds to on claim 29, with all its recitation. For at least the same reason as claim 29, claim 29 is therefore also patentable over Westerninen, and in further view of Cooper.

### **Conclusion**

In conclusion, claims 1-30 are in condition for allowance. Early issuance of Notice of Allowance is respectfully requested.

The Commissioner is hereby authorized to charge shortages or credit overpayments to Deposit Account No. 500393.

Respectfully submitted,  
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